

Features

- Single-Supply Operation from +2.1V ~ +5.5V
- Rail-to-Rail Input / Output
- Gain-Bandwidth Product: 150KHz (Typ)
- Low Input Bias Current: 1pA (Typ)
- Low Offset Voltage: 3.5mV (Max)
- Quiescent Current: 5.5µA per Amplifier (Typ)
- Operating Temperature: -40°C ~ +125°C

General Description

- Embedded RF Anti-EMI Filter
- Small Package:

GS8521 Available in SOT23-5 Package GS8522 Available in SOP-8 and MSOP-8 Packages GS8524 Available in SOP-14 and TSSOP-14 Packages

The GS852X family have a high gain-bandwidth product of 150KHz, a slew rate of $0.07V/\mu$ s, and a quiescent current of 5.5μ A/amplifier at 5V. The GS852X family is designed to provide optimal performance in low voltage and low noise systems. They provide rail-to-rail output swing into heavy loads. The input common mode voltage range includes ground, and the maximum input offset voltage is 3.5mV for GS852X family. They are specified over the extended industrial temperature range (-40 °C to +125 °C). The operating range is from 2.1V to 5.5V. The GS8521 single is available in Green SOT-23-5 packages The GS8522 Dual is available in Green SOP-8 and MSOP-8 packages. The GS8524 Quad is available in Green SOP-14 and TSSOP-14 packages.

Applications

- ASIC Input or Output Amplifier
- Sensor Interface
- Medical Communication
- Smoke Detectors

Pin Configuration

- Audio Output
- Piezoelectric Transducer Amplifier
- Medical Instrumentation
- Portable Systems



Figure 1. Pin Assignment Diagram







Absolute Maximum Ratings

Condition	Min	Мах			
Power Supply Voltage (V _{DD} to Vss)	-0.5V	+7.5V			
Analog Input Voltage (IN+ or IN-)	Vss-0.5V	V _{DD} +0.5V			
PDB Input Voltage	Vss-0.5V	+7V			
Operating Temperature Range	-40°C	+125°C			
Junction Temperature	+16	0°C			
Storage Temperature Range	-55°C	+150°C			
Lead Temperature (soldering, 10sec)	+26	+260°C			
Package Thermal Resistance (T _A =+25℃)					
SOP-8, θ _{JA}	125°	C/W			
MSOP-8, θ _{JA}	216°	C/W			
SOT23-5, θ _{JA}	190°	190°C/W			
ESD Susceptibility					
НВМ	64	ίν.			
MM	30	0V			

Note: Stress greater than those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions outside those indicated in the operational sections of this specification are not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Package/Ordering Information

MODEL	CHANNEL	ORDER NUMBER	PACKAGE DESCRIPTION	PACKAGE OPTION	MARKING INFORMATION	
GS8521	Single	GS8521-TR	SOT23-5	Tape and Reel,3000	8521	
000500	Doorsoo Dual	GS8522-SR	GS8522-SR	SOP-8	Tape and Reel,4000	GS8522
GS8522 Dual	GS8522-MR	MSOP-8	Tape and Reel,3000	GS8522		
000524	Oursel	GS8524-TR	TSSOP-14	Tape and Reel,3000	GS8524	
GS8524 Quad	Quad	GS8524-SR	SOP-14	Tape and Reel,2500	GS8524	







Electrical Characteristics

(At Vs = +5V, RL = $500k\Omega$ connected to Vs/2, and Vout = Vs/2, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	ТҮР	MIN	мах	UNITS
INPUT CHARACTERISTICS					1	
Input Offset Voltage	V _{os}	$V_{CM} = V_S/2$	0.4		3.5	mV
Input Bias Current	IB		1			pА
Input Offset Current	los		1			pА
Common-Mode Voltage Range	V _{CM}	V _S = 5.5V	-0.1 to +5.6			V
Common Made Dejection Datio	CMDD	$V_{\rm S}$ = 5.5V, $V_{\rm CM}$ = -0.1V to 4V	114	70		dB
Common-Mode Rejection Ratio	CMRR	$V_{\rm S}$ = 5.5V, $V_{\rm CM}$ = -0.1V to 5.6V	87	60		uв
Onen Leen Veltage Cein		$R_L = 500 k\Omega$, $V_O = +0.1 V$ to +4.9V	110	90		dB
Open-Loop Voltage Gain	A _{OL}	R_L = 100k Ω , V_O = +0.1V to +4.9V	108	88		uв
Input Offset Voltage Drift	$\Delta V_{OS} / \Delta_T$		2			uV/ºC
OUTPUT CHARACTERISTICS						
Output Voltage Swing from Rail	V _{OH}	R _L = 500kΩ	4.997	4.990		V
	V _{OL}	$R_L = 500 k\Omega$	3	10		mV
0 1 1 0 1	ISOURCE		58	40		
Output Current	I _{SINK}	$R_L = 10\Omega$ to $V_S/2$	58	40		mA
POWER SUPPLY	66					
Operating Voltage Range				2.1	5.5	V
Power Supply Rejection Ratio	PSRR	$V_{\rm S}$ = +2.5V to +5.5V, $V_{\rm CM}$ = +0.5V	94	65		dB
Quiescent Current / Amplifier	Ιq		5.5			uA
DYNAMIC PERFORMANCE						
Gain-Bandwidth Product	GBP		150			kHz
Slew Rate	SR	G = +1, 2V Output Step	0.07			V/uS
Settling Time to 0.1%	ts	G = +1, 2V Output Step	30			uS
NOISE PERFORMANCE		1				
		f = 1kHz	85			nV/\sqrt{Hz}
Voltage Noise Density	en	f = 10kHz	44			nV / \sqrt{Hz}







Typical Performance characteristics



At T_A=+25°C, V_S=+5V, and R_L=500K Ω connected to V_S/2, unless otherwise noted.

March 2020-REV_V0



Application Note

Size

GS852X family series op amps are unity-gain stable and suitable for a wide range of general-purpose applications. The small footprints of the GS852X family packages save space on printed circuit boards and enable the design of smaller electronic products.

Power Supply Bypassing and Board Layout

GS852X family series operates from a single 2.1V to 5.5V supply or dual $\pm 1.05V$ to $\pm 2.75V$ supplies. For best performance, a 0.1µF ceramic capacitor should be placed close to the V_{DD} pin in single supply operation. For dual supply operation, both V_{DD} and V_{SS} supplies should be bypassed to ground with separate 0.1µF ceramic capacitors.

Low Supply Current

The low supply current (typical 5.5µA per channel) of GS852X family will help to maximize battery life. They are ideal for battery powered systems.

Operating Voltage

GS852X family operates under wide input supply voltage (2.1V to 5.5V). In addition, all temperature specifications apply from -40 °C to +125 °C. Most behavior remains unchanged throughout the full operating voltage range. These guarantees ensure operation throughout the single Li-Ion battery lifetime.

Rail-to-Rail Input

The input common-mode range of GS852X family extends 100mV beyond the supply rails (V_{SS} -0.1V to V_{DD} +0.1V). This is achieved by using complementary input stage. For normal operation, inputs should be limited to this range.

Rail-to-Rail Output

Rail-to-Rail output swing provides maximum possible dynamic range at the output. This is particularly important when operating in low supply voltages. The output voltage of GS852X family can typically swing to less than 10mV from supply rail in light resistive loads (>500k Ω), and 30mV of supply rail in moderate resistive loads (100k Ω).

Capacitive Load Tolerance

The GS852X family is optimized for bandwidth and speed, not for driving capacitive loads. Output capacitance will create a pole in the amplifier's feedback path, leading to excessive peaking and potential oscillation. If dealing with load capacitance is a requirement of the application, the two strategies to consider are (1) using a small resistor in series with the amplifier's output and the load capacitance and (2) reducing the bandwidth of the amplifier's feedback loop by increasing the overall noise gain. Figure 2. shows a unity gain follower using the series resistor strategy. The resistor isolates the output from the capacitance and, more importantly, creates a zero in the feedback path that compensates for the pole created by the output capacitance.



Figure 2. Indirectly Driving a Capacitive Load Using Isolation Resistor







The bigger the R_{ISO} resistor value, the more stable V_{OUT} will be. However, if there is a resistive load R_L in parallel with the capacitive load, a voltage divider (proportional to R_{ISO}/R_L) is formed, this will result in a gain error.

The circuit in Figure 3 is an improvement to the one in Figure 2. R_F provides the DC accuracy by feed-forward the V_{IN} to R_L. C_F and R_{ISO} serve to counteract the loss of phase margin by feeding the high frequency component of the output signal back to the amplifier's inverting input, thereby preserving the phase margin in the overall feedback loop. Capacitive drive can be increased by increasing the value of C_F. This in turn will slow down the pulse response.



Figure 3. Indirectly Driving a Capacitive Load with DC Accuracy







Typical Application Circuits

Differential amplifier

The differential amplifier allows the subtraction of two input voltages or cancellation of a signal common the two inputs. It is useful as a computational amplifier in making a differential to single-end conversion or in rejecting a common mode signal. Figure 4. shown the differential amplifier using GS852X family.



Figure 4. Differential Amplifier

$$V_{\text{OUT}} = \left(\frac{R_1 + R_2}{R_3 + R_4}\right) \frac{R_4}{R_1} V_{\text{IN}} - \frac{R_2}{R_1} V_{\text{IP}} + \left(\frac{R_1 + R_2}{R_3 + R_4}\right) \frac{R_3}{R_1} V_{\text{REF}}$$

If the resistor ratios are equal (i.e. $R_1=R_3$ and $R_2=R_4$), then

$$V_{\rm OUT} = \frac{R_2}{R_1} (V_{\rm IP} - V_{\rm IN}) + V_{\rm REF}$$

Low Pass Active Filter

The low pass active filter is shown in Figure 5. The DC gain is defined by $-R_2/R_1$. The filter has a -20dB/decade roll-off after its corner frequency $f_c=1/(2\pi R_3 C_1)$.



Figure 5. Low Pass Active Filter







Instrumentation Amplifier

The triple GS852X family can be used to build a three-op-amp instrumentation amplifier as shown in Figure 6. The amplifier in Figure 6 is a high input impedance differential amplifier with gain of R2/R1. The two differential voltage followers assure the high input impedance of the amplifier.









Package Information

MSOP-8







		Dimensions In Inches		
MIN	MAX	MIN	MAX	
0.820	1.100	0.032	0.043	
0.020	0.150	0.001	0.006	
0.750	0.950	0.030	0.037	
0.250	0.380	0.010	0.015	
0.090	0.230	0.004	0.009	
2.900	3.100	0.114	0.122	
2.900	3.100	0.114	0.122	
4.750	5.050	0.187	0.199	
0.650	BSC	0.026	BSC	
0.400	0.800	0.016	0.031	
0°	6°	0°	6°	
	In Milli MIN 0.820 0.020 0.750 0.250 0.090 2.900 2.900 4.750 0.650 0.400	0.820 1.100 0.020 0.150 0.750 0.950 0.250 0.380 0.090 0.230 2.900 3.100 2.900 3.100 4.750 5.050 0.650 BSC 0.400 0.800	In Millimeters In In MIN MAX MIN 0.820 1.100 0.032 0.020 0.150 0.001 0.750 0.950 0.030 0.250 0.380 0.010 0.090 0.230 0.004 2.900 3.100 0.114 2.900 3.100 0.114 4.750 5.050 0.026 0.650 BSC 0.026 0.400 0.800 0.018	







SOP-8



Symbol		nsions meters	Dimensions In Inches		
	MIN	MAX	MIN	MAX	
А	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
с	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
e	1.27 BSC		0.050	BSC	
L	0.400	1.270	0.016	0.050	
0	0°	8°	0°	8°	







SOT23-5

GS8521/22/24

- θ

Ŀ

- 0.2

с



Symbol	Dimen In Milli	isions imeters	Dimensions In Inches		
	MIN	MAX	MIN	MAX	
А	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
с	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E	1.500	1.700	0.059	0.067	
E1	2.650	2.950	0.104	0.116	
e	0.950	BSC	0.037 BSC		
e1	1.900 BSC		0.075	BSC	
L	0.300	0.600	0.012	0.024	
e	0°	8°	0°	8°	







TSSOP-14



Symbol -	Dimensions In	Millimeters	Dimensions In Inches		
	Min	Max	Min	Max	
D	4.900	5.100	0.193	0.201	
E	4.300	4.500	0.169	0.177	
ь	0.190	0.300	0.007	0.012	
с	0.090	0.200	0.004	0.008	
E1	6.250	6.550	0.246	0.258	
А	1.200			0.047	
A2	0.800	1.000	0.031	0.039	
A1	0.050	0.150	0.002	0.006	
e	0.65 (BSC)		0.026	(BSC)	
L	0.500	0.700	0.020	0.028	
Н	0.25(TYP)		0.01(TYP)		
θ	1° 7°		1°	7 °	





GS8521/22/24

GAINSIL







RECOMMENDED LAND PATTERN (Unit: mm)





Symbol	Dimens	Dimensions In Millimeters			Dimensions In Inches		
Symbol	MIN	MOD	MAX	MIN	MOD	MAX	
А	1.35		1.75	0.053		0.069	
A1	0.10		0.25	0.004		0.010	
A2	1.25		1.65	0.049		0.065	
A3	0.55		0.75	0.022		0.030	
b	0.36		0.49	0.014		0.019	
D	8.53		8.73	0.336		0.344	
E	5.80		6.20	0.228		0.244	
E1	3.80		4.00	0.150		0.157	
e		1.27 BSC		0.050 BSC			
L	0.45		0.80	0.018		0.032	
L1		1.04 REF		0.040 REF			
L2		0.25 BSC		0.01 BSC			
R	0.07			0.003			
R1	0.07			0.003			
h	0.30		0.50	0.012		0.020	
θ	0°		8°	0°		8°	



